



FIG. 2

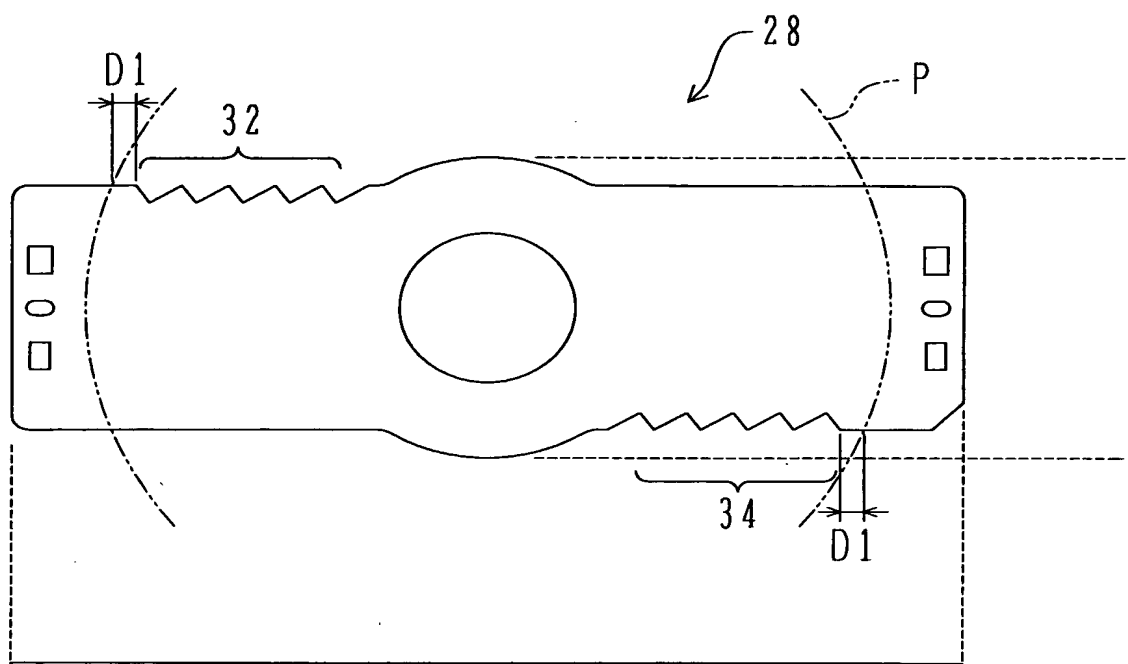


Fig. 1 is a cross-sectional view of a semiconductor device. It shows a substrate 22 with a layer 40 on top. A central region 26 is defined by vertical lines 42 and 42b. A layer 42a is shown below 40. A layer 46a is on the right. Dimensions 51, 51a, 51b, and 51c are indicated at the top.

FIG. 4

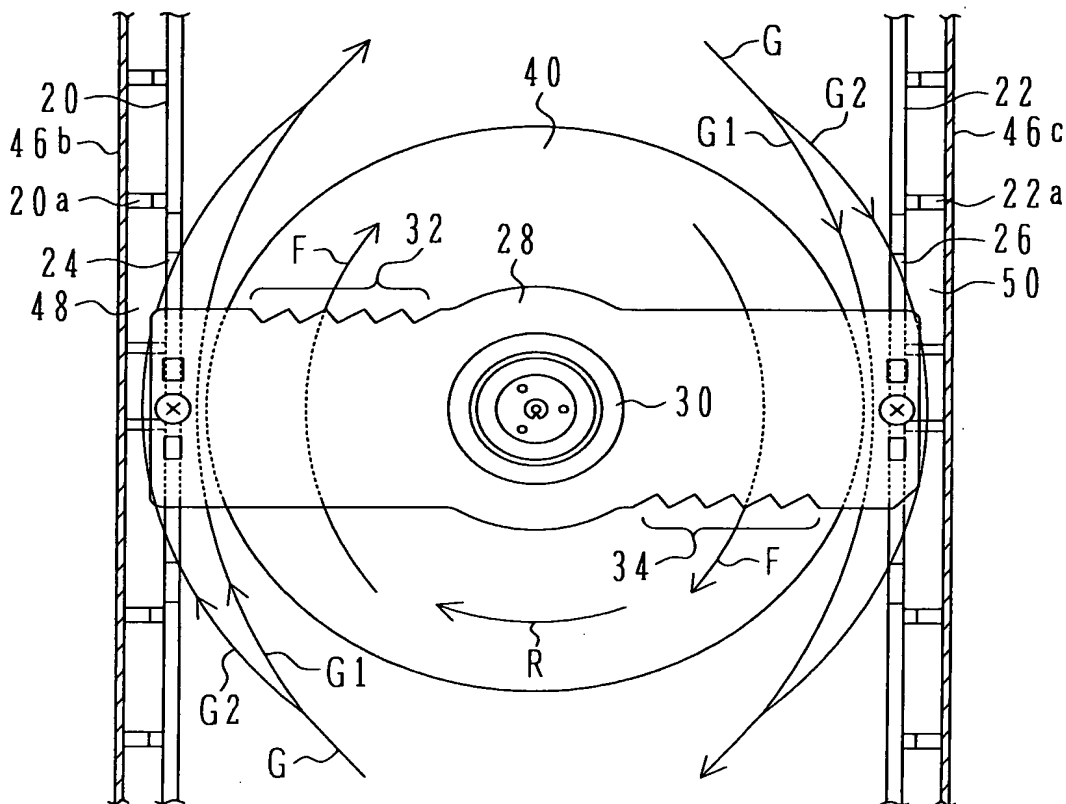


FIG. 5

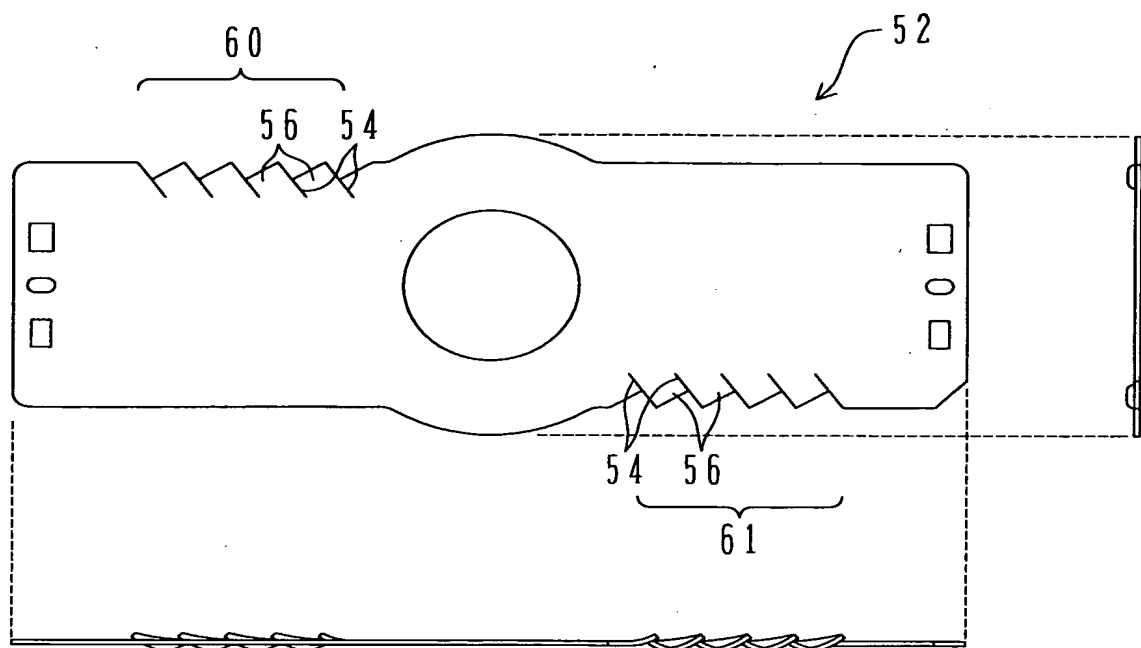


FIG. 6

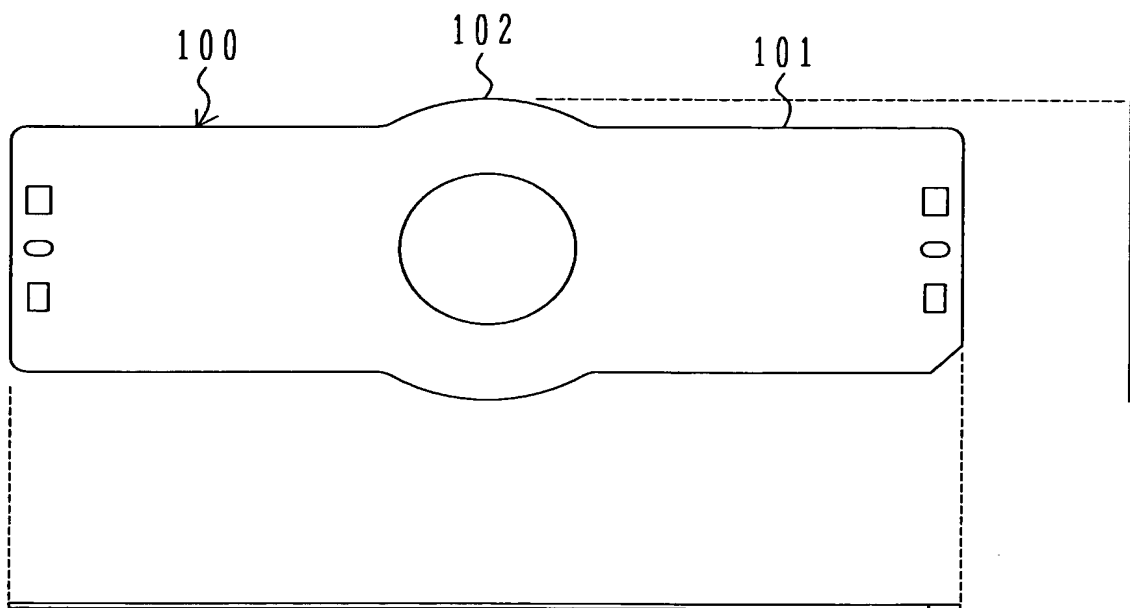


FIG. 7

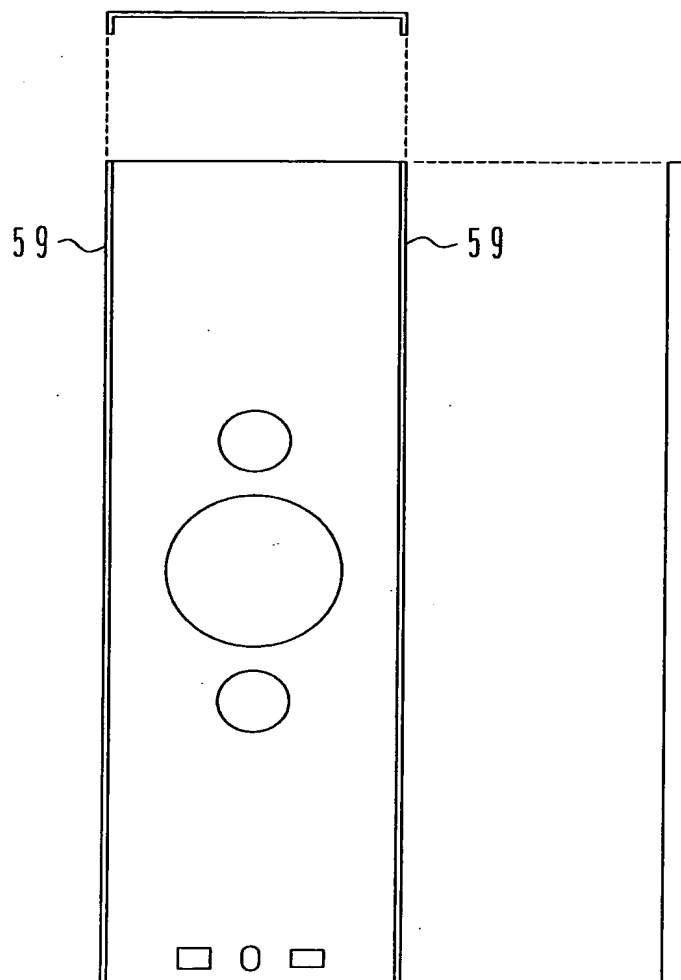


FIG. 8

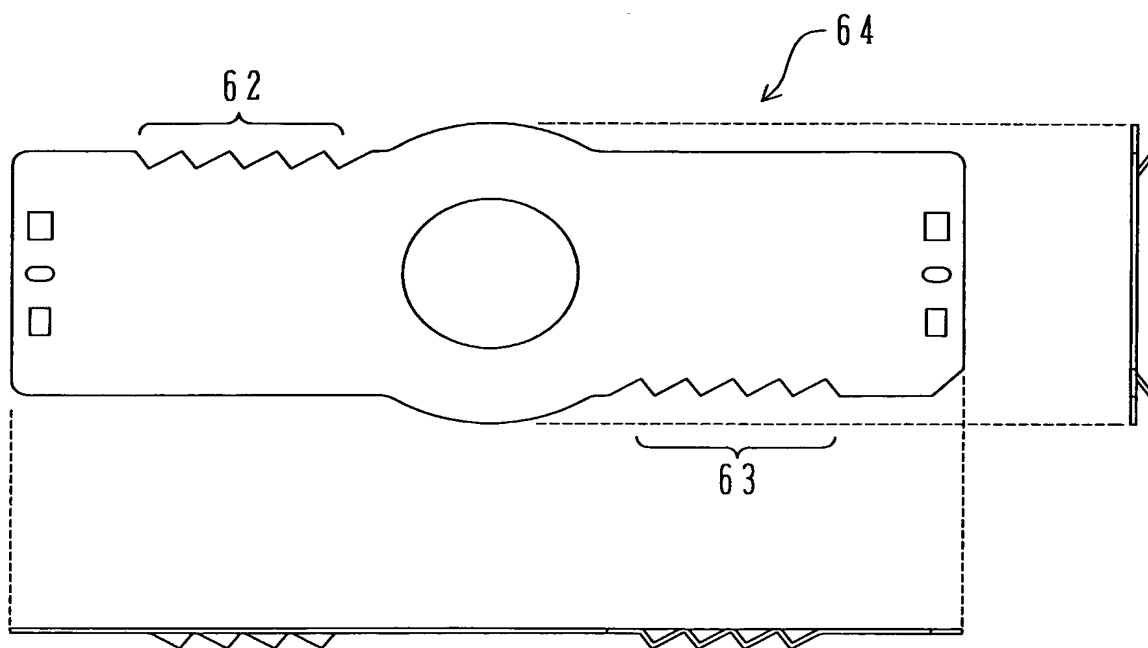




FIG. 9A

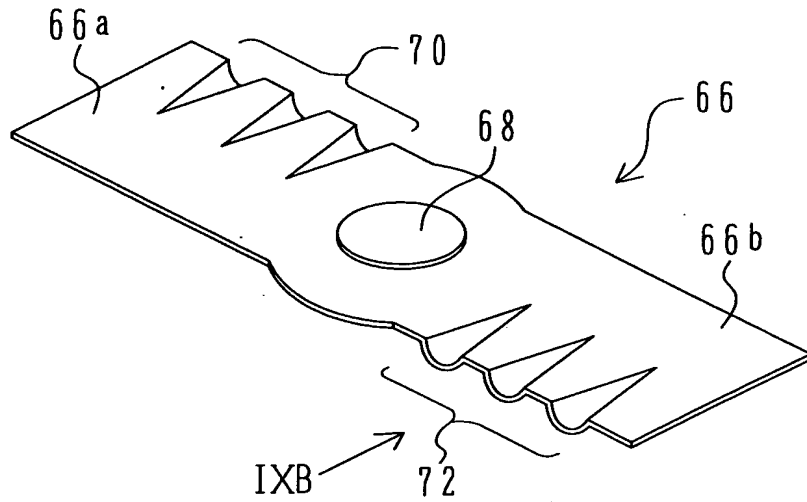


FIG. 9B

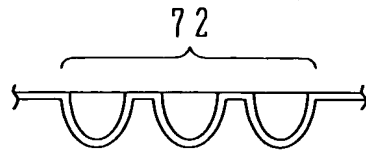


FIG. 10A

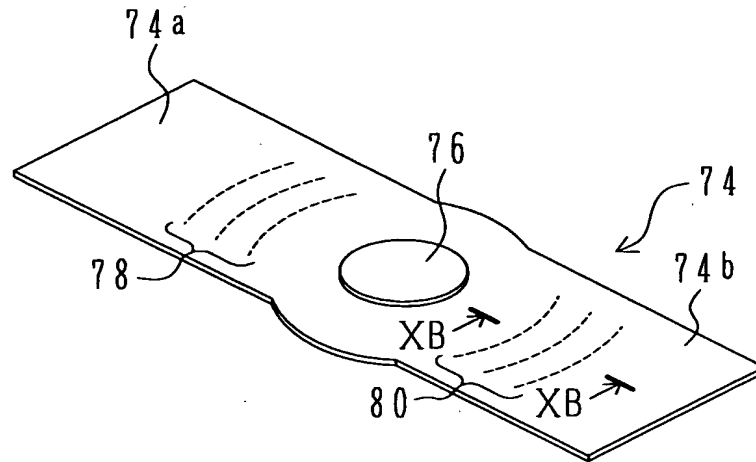


FIG. 10B

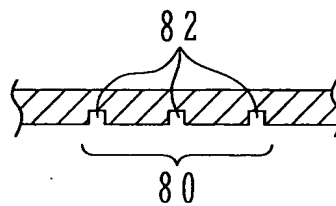


FIG. 11

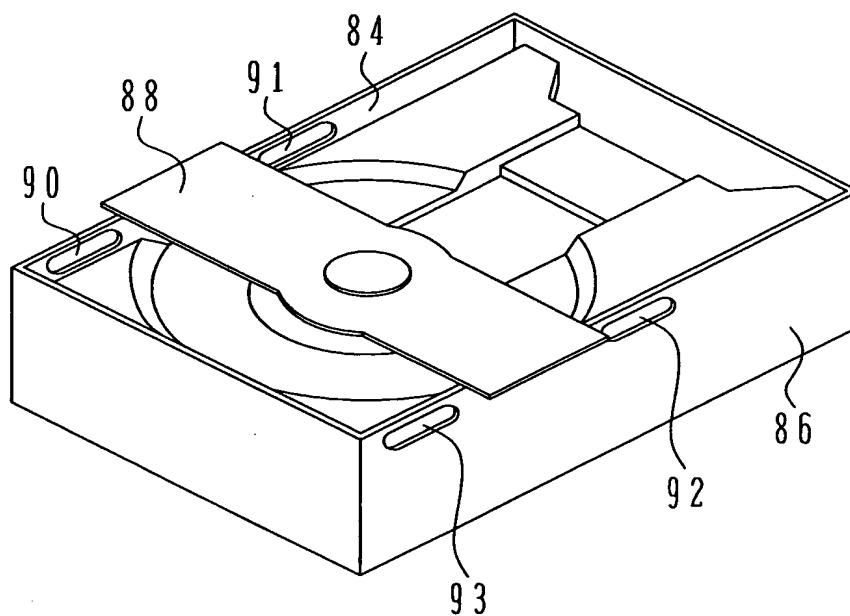


FIG. 12

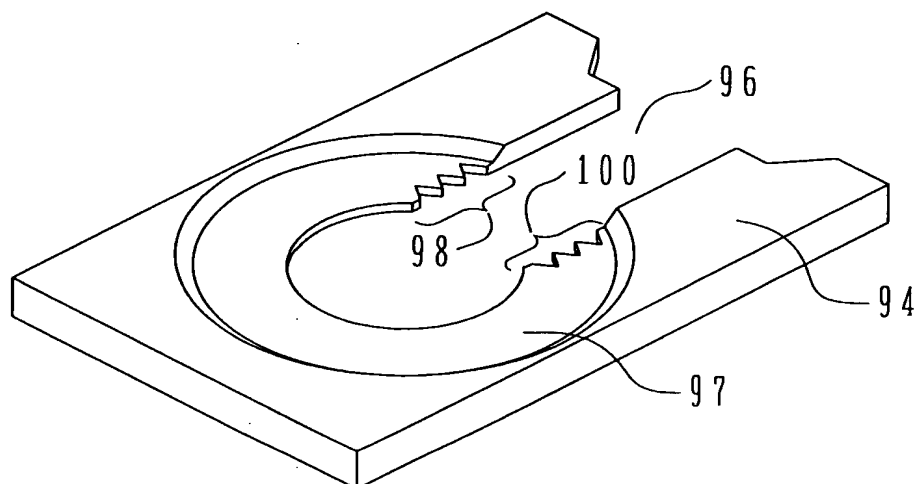


FIG. 13A

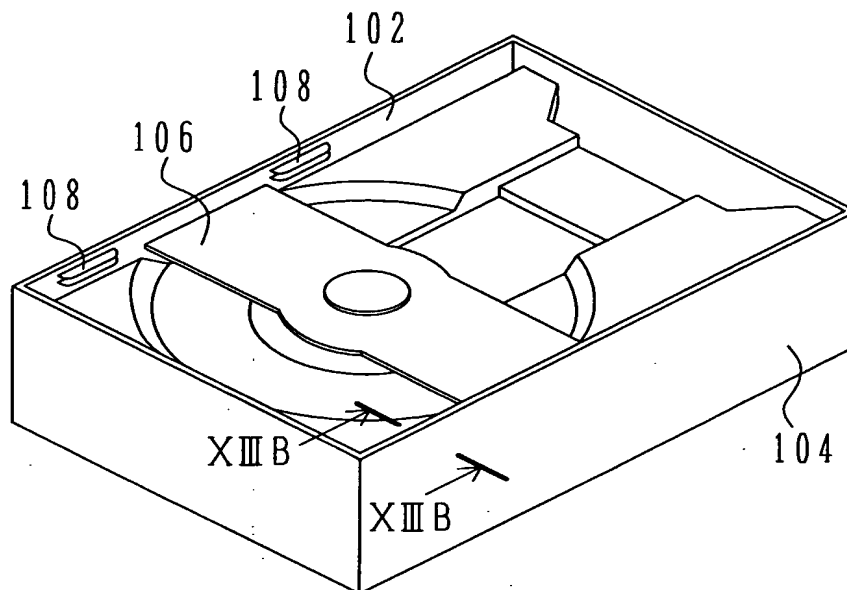


FIG. 13B

